

**EAST - [default.wsp:1]**

File View Edit Tools Window Help

**Active**

- L1: (140473) (pll (phase near8 loop))
- L2: (126818) (phase near8 (comparator compar\$4))
- L3: (37116) 1 and 2
- L4: (13248) 3 and (phase near8 shift\$3)
- L5: (159560) (width near8 (determin\$4 detect\$3))
- L6: (1212) 4 and 5
- L7: (713) 369/47.28
- L8: (24827) G11B005/09
- L9: (1928) 331/16
- L10: (3802) 331/17
- L11: (10324) H03L007/00
- L13: (8753) H03L007/06
- L12: (1573) 327/159
- L14: (16) 327/159
- L15: (46) 331/16
- L16: (100) 331/17
- L17: (48) 369/47.28

**Failed**

**Search**

DBs USPAT OR  Plurals  Highlight all hits terms initially

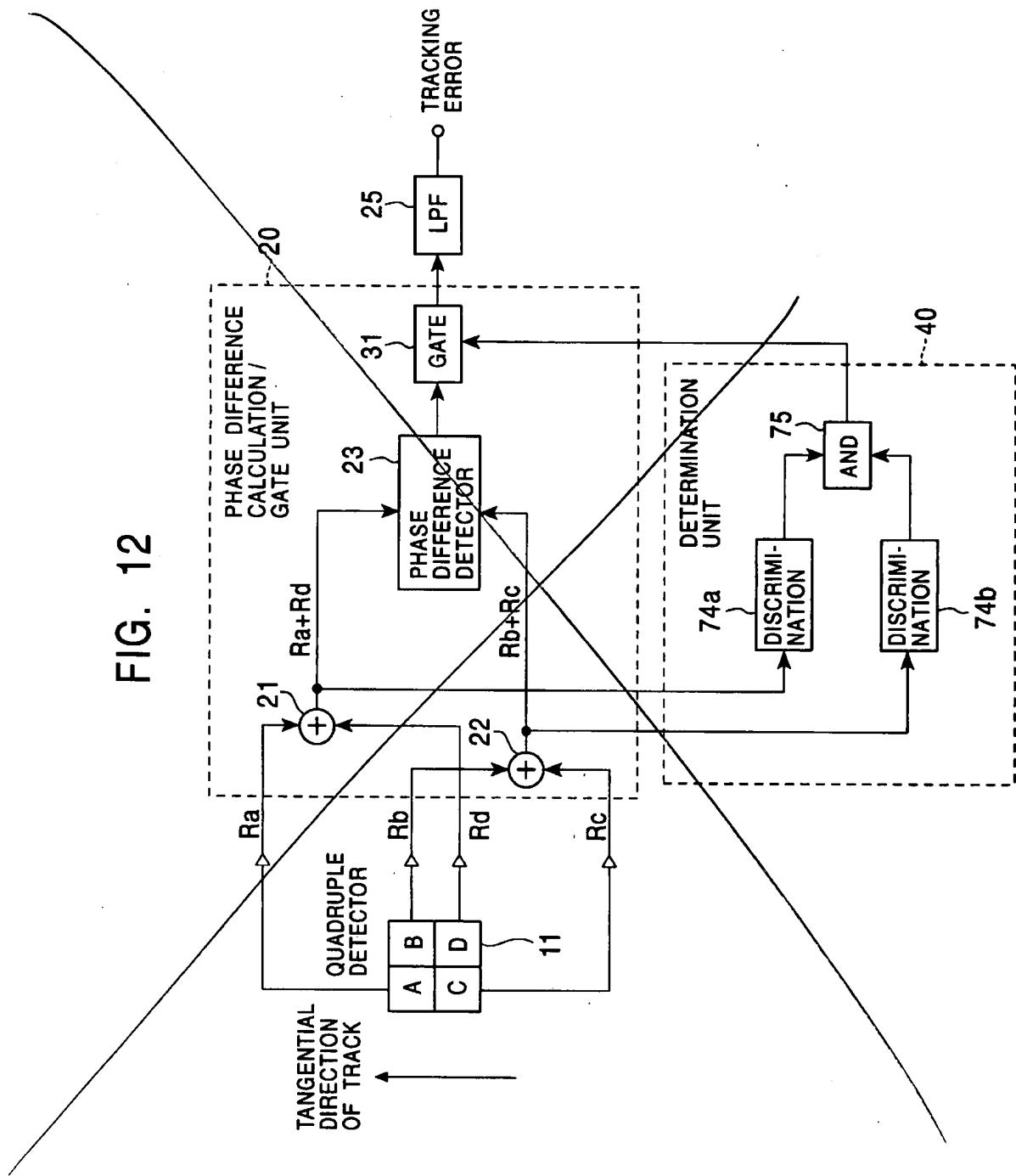
for 10/824264

Type	L #	Hits	Search Text	DBs	Time	Stam	Comment	Error	Definit	Er
1	BRS	L1	140473 (pll (phase near8 3 . . . loop))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1	6:15:32				
2	BRS	L2	126818 (phase near8 8 . . . (comparator compar\$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1	6:15:32				
3	BRS	L3	371161 and 2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1	6:15:32				
4	BRS	L4	132483 and (phase near8 shift\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1	6:15:33				
5	BRS	L5	159560 (width near8 0 . . . (determin\$4 detect\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1	6:15:33				
6	BRS	L6	1212 4 and 5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1	6:16:07				
7	BRS	L7	713 369/47.28	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1	6:16:07				
8	BRS	L8	24827 G11B005/09	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1	6:16:07				
9	BRS	L9	1928 331/16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1	6:16:07				
10	BRS	L10	3802 331/17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1	6:16:07				
11	BRS	L11	10324 H03L007/00	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1	6:16:08				
12	BRS	L13	8753 H03L007/06	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1	6:16:08				

Hits Details HTML

Ready

FIG. 12



EAST - [default.wsp:1] X

File View Edit Tools Window Help

Active  Failed  All  Recent  Search  Details  HTML

L1: (140473) (pll (phase near8 loop))  
 L2: (126818) (phase near8 (comparator compar\$4))  
 L3: (37116) 1 and 2  
 L4: (13248) 3 and (phase near8 shift\$3)  
 L5: (159560) (width near8 (determin\$4 detect\$3))  
 L6: (1212) 4 and 5  
 L7: (713) 369/47.28  
 L8: (24827) G11B005/09  
 L9: (1928) 331/16  
 L10: (3802) 331/17  
 L11: (10324) H03L007/00  
 L13: (8753) H03L007/06  
 L12: (1573) 327/159  
 L14: (16) 327/159  
 L15: (46) 331/16  
 L16: (100) 331/17  
 L17: (48) 369/47.28

Shorten  List  Browse  Cluster  Clear  
 DBs  USPAT  Plurals  
 Default operator: OR  Highlight all hit terms initially

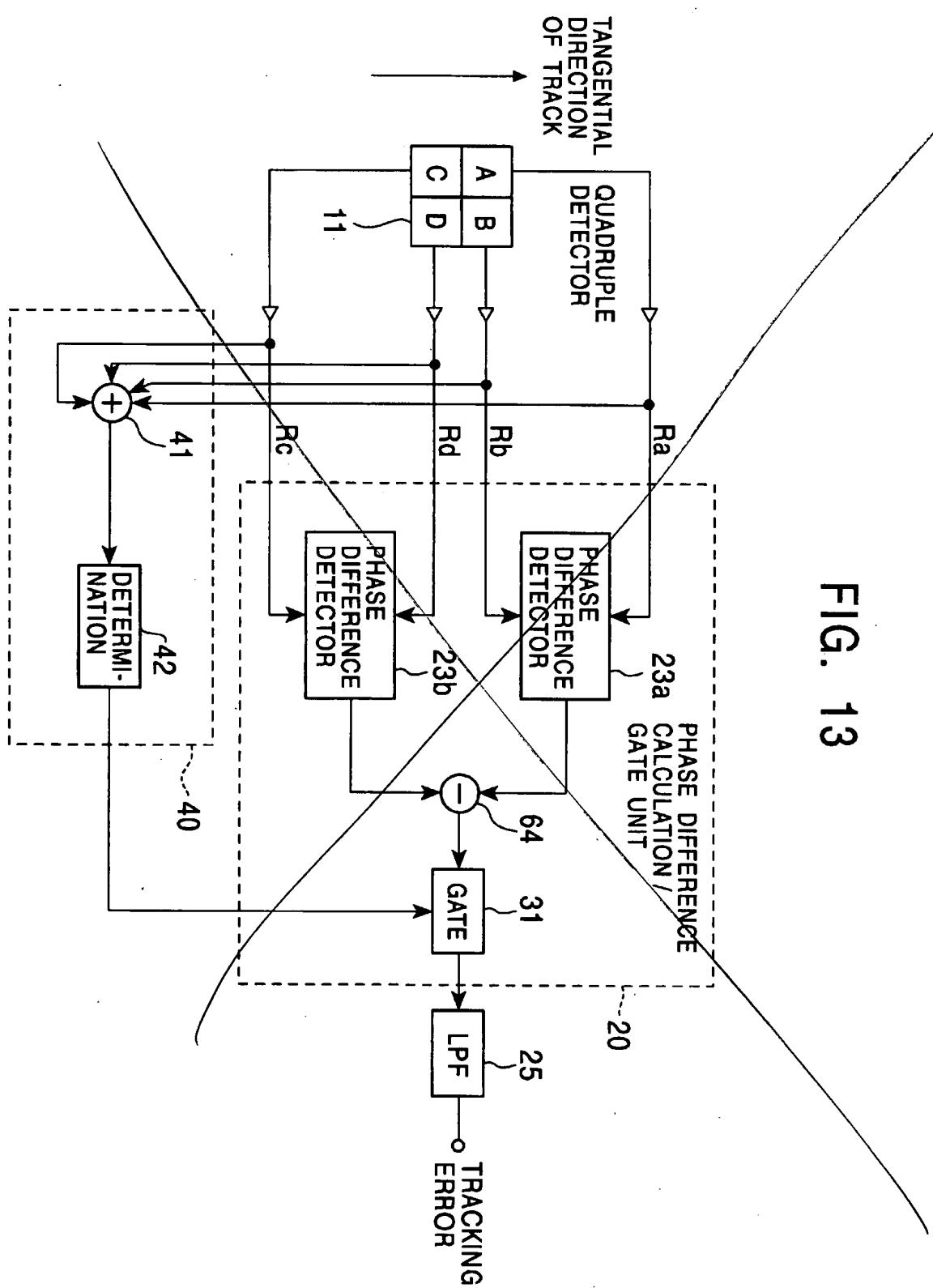
BRS form  I&R form  Image  Text  HTML

Type	L #	Hits	Search Text	DBs	Time	Stam	Comment	Error	Definit	Ex
7	BRS	L7	713 369/47.28	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1:6 16:07					
8	BRS	L8	24827G11B005/09	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1:6 16:07					
9	BRS	L9	1928 331/16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1:6 16:07					
10	BRS	L10	3802 331/17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1:6 16:07					
11	BRS	L11	10324H03L007/00	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1:6 16:08					
12	BRS	L13	8753 H03L007/06	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1:6 16:08					
13	BRS	L12	1573 327/159	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1:6 16:09					
14	BRS	L14	16 327/159	US-PGPUB	2007/11/1:6 16:09					
15	BRS	L15	46 331/16	US-PGPUB	2007/11/1:6 16:09					
16	BRS	L16	100 331/17	US-PGPUB	2007/11/1:6 16:09					
17	BRS	L17	48 369/47.28	US-PGPUB	2007/11/1:6 16:09					

Hits  Details  HTML

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FIG. 13



## EAST - [default.wsp:1]

File View Edit Tools Window Help

 Drafts Pending Active

- L1: (11136) (pulse time) adj5 (width near8 detect\$4)
- L2: (234249) (phase near8 shift\$4)
- L3: (1064) 1 and 2
- L4: (17402) (count\$3 near8 (frequency adj5 divid\$3))
- L5: (106) 3 and 4

 Failed Saved

- S1: (5) "7085204"

 Favorites Tagged (0) Groups    List    Windows    Quotes    Clear QBe    USPAT

Default operator: OR

 Plurals Highlight all hit terms initially BRS form    IS&R form    Image    Text    HTML

Type	L #	Hits	Search Text	DBS	Time	Stam	Comment	Error	Definit	Ex
1 BRS	L1	11136	(pulse time) adj5 (width near8 detect\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	2007/11/1 5 12:44					
2 BRS	L2	234249	(phase near8 shift\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	2007/11/1 5 12.45					
3 BRS	L3	1064	1 and 2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	2007/11/1 5 12:45					
4 BRS	L4	17402	(count\$3 near8 (frequency adj5 divid	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	2007/11/1 5 12:46					
5 BRS	L5	106	3 and 4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	2007/11/1 5 12:46					

 Hits    Details    HTML

Ready

## EAST - [default.wsp:1]

File View Edit Tools Window Help

Pending Active

- L1: (11136) (pulse time) adj5 (width near8 detect\$4)
- L2: (234249) (phase near8 shift\$4)
- L3: (1064) 1 and 2
- L4: (17402) (count\$3 near8 (frequency adj5 divid\$3))
- L5: (106) 3 and 4
- L6: (125826) (pll (phase adj5 loop))
- L7: (1239) 1 and 6
- L8: (127) 4 and 7
- L9: (82) 2 and 8
- L10: (317) 6 same 1
- L11: (20) 4 and 10

Search Lot Browse Show Clear

DBs: US-PGPUB-USPAT-USOCR-FPRS EPO, JPO, DERWENT-IBM\_TDB

Default operator: OR

4 and 10

BRS form IS&R form Image Text HTML

U	I	Document ID	Issue Date	Page	Title	Current OR	Current XR	Retrieval	Inventor	S	C	P	E
5	<input checked="" type="checkbox"/>	US 7180377	20070220	15	Method and apparatus for a hybrid phase lock	331/17	327/157;		Leong; Colin Wai	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	B1					331/DIG.2		Mun, et al.				
6	<input checked="" type="checkbox"/>	US 7177611	20070213	16	Hybrid control of phase locked loops.	455/260	331/17;		Goldman; Stanley Jav.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
7	<input checked="" type="checkbox"/>	US 7084709	20060801	15	Hybrid analog/digital	331/11	327/156;		Leong; Colin Wai	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	B1					327/157;		Mun, et al.				
8	<input checked="" type="checkbox"/>	US 6580775	20030617	6	Method of detecting frequency of digital ph	375/375	327/151;		Park; Hyun-Soo et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
9	<input checked="" type="checkbox"/>	US 6518845	20030211	13	PLL frequency synthesizer circuit	331/16	327/152;		Nakamichi; Hiroto	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
10	<input checked="" type="checkbox"/>	US 6441691	20020827	16	PLL cycle slip compensation	331/25	327/12;		Jones; Theron et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	B1					331/156						
11	<input checked="" type="checkbox"/>	US 6392641	20020521	23	PLL circuit for digital display apparatus	345/213	331/1A;		Nishimura; Eizo et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	B1					331/20;						
12	<input checked="" type="checkbox"/>	US 5783972	19980721	14	Power saving PLL circuit	331/17	327/157;		Nishikawa; Masato	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	A					327/159;						
13	<input checked="" type="checkbox"/>	US 5661425	19970826	24	Digital PLL circuit	327/159	327/147;		Minoda; Hidenori et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	A					327/150;						
14	<input checked="" type="checkbox"/>	US 5548249	19960820	34	Clock generator and method for generating a	331/1A	327/107;		Sumita; Masaya et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	A					327/150;						
15	<input checked="" type="checkbox"/>	US 4996596	19910226	28	Phase synchronizing circuit in video signal	348/542	348/E5.021		Hirao; Yoshichika et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	A											
16	<input checked="" type="checkbox"/>	US 4727530	19880223	29	Disc rotation control device for a disc slave	369/47.45	369/111;		Tomisawa; Norio	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	A					386/86;						
17	<input checked="" type="checkbox"/>	US 4707856	19871117	12	AM stereo receiver	381/15	324/76.47;		Tanaka; Kanji et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	A					324/76.55;						
18	<input checked="" type="checkbox"/>	US 4688246	19870818	14	CATV scrambling system with compressed digital	380/237	348/E7.028		Eilers; Carl G. et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	A											
19	<input checked="" type="checkbox"/>	US 3868677	19750225	12	PHASE-LOCKED VOLTAGE-TO-DIGITAL CONV	341/157	341/158		Kidd; Marshall C. ERKKU, J H et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	A											
20	<input checked="" type="checkbox"/>	GB 2194714	19880309	11	Phase locked loop for ISDN in telephone syste								
	<input checked="" type="checkbox"/>	A											

Hits Details HTML

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